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| **Project:** | **Cor\_IF + 4-wire** |
| **Document under Review:** | **4WCTW\_Modbus Slave Subsystem Requirement Specification V1.1.doc** |
| **Revision:** | **1.1** |
| **Review Date:** | **15.06.2016** |

**Review-Participant:**

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| --- | --- | --- |
| *Dept.* | *Name* |  |
| R&D | Georg Horst |  |
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**Decision of the Review:**

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| --- | --- | --- |
|  | *Decision* | *next steps* |
| x | Inspection passed ***without restrictions*** | Phase finished |
|  | Inspection passed ***with restrictions*** | some changes must be done |
|  | Inspection ***not*** passed | Inspection must be repeated |

**Changes are proved:**

The Reviewer confirms that all changes are done:

|  |  |  |
| --- | --- | --- |
| proved Rev: | Date: | Reviewer: |
| 1.2 | 26.07.16 | G. Horst |

**Check list:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
|  |  | yes | no | N.A. |
| 1. | Does the specification explain the currently known requirements that the product finally has to perform? |  | **x** |  |
| 2. | Are complex subjects explained? | **x** |  |  |
| 3. | Are all used abbreviations and technical expressions explained? | **x** |  |  |
| 4. | Are all referenced documents in the reference list? | **x** |  |  |
| 5. | Contains the document a rough overview of the product’s main functionality? | **x** |  |  |
| 6. | Are all requirements referenced? | **x** |  |  |
| 7. | Are all requirements testable? | **X** |  |  |
| 8. | Is the typical environment of the product described? | **x** |  |  |
| 9. | Is defined which components already exist, will be bought or self-written? |  |  | **x** |
| 10. | Are safety requirements (e.g error-detection, error-handling) specified? |  |  | **x** |
| 11. | Are service/update requirements described? |  |  | **x** |
| 12. | Are all dependencies to other requirement documents considered? |  | **x** |  |
| 13. | Are templates used in a correct manner? | **x** |  |  |
| 14. | Are all open issues transferred to the defects table? | **x** |  |  |

**Remarks:**

**Defects**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| No. | Checkpoint | Description | Major Defect | done  Date |
| 1 | 1 | 1.1 Scope (site 3):  Subsystem is also designed for the Enhanced version, not only for MINT |  | 26.07.2016 |
| 2 | 1 | Req. 4.1.1:  Modbus Slave will be also implemented at Motherboard. Depends on the device variant |  | 26.07.2016 |
| 3 | 12 | Req. 4.2.2:  The Source for this Requirements is the 4-wire TRS |  | 26.07.2016 |
| 4 | 12 | Req. 4.2.3:  Default of Modbus Baudrate is 9600 and not 19200.  Further the Source for this Requirements is the 4-wire TRS |  | 26.07.2016 |
| 5 | 1 | Req. 4.3.3:  Parameters behind the Service level are not accessible for MinT and Enhanced |  | 26.07.2016 |
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